

REALITY Workshop

22nd June 2010



Agenda morning

- 9:00 Welcome to the Workshop
Yves Laplanche (ARM)
- 9:10 The REALITY project in a nut-shell
Miguel Miranda (imec)
- 10:00 Technology Variability Modelling
Asen Asenov (U.Glasgow)
- 10:30 Circuit to System Variability Modelling
Paul Zuber (imec)
- 11:20 IC Design for Reliability in Nanometre CMOS
Elie Maricau, Bram Rooselaar (U. Leuven)
- 11:50 System Design for Variability (HW and SW)
Francesco Paterna, Andrea Acquaviva (U.Bologna)

Agenda Afternoon

- 13:40 DEMO
Physical IP to Core level flow integration and benchmarking
Yves Laplanche (ARM) & Paul Zuber (imec)
- 15:00 DEMO
Core level to System level flow integration and benchmarking
Francesco Patterna, (U.Bologna)
- 16:20 Open project discussion: audience feedback
Yves Laplanche (ARM)
- 16:50 Conclusions from the workshop
Miguel Miranda (imec)